

Introduction to the Project

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The National Semiconductor Human Resource Development Program (NSHRDP) – Phase-I, under the guidance of the Government of Pakistan, invites proposals from qualified and experienced vendors for the supply, installation, configuration, and technical support of a comprehensive suite of hardware and software infrastructure. The purpose of this procurement is to establish advanced semiconductor training labs across the selected academic and research institutions to equip undergraduate, graduate, and professional learners with practical skills in integrated circuit (IC) and system-on-chip (SoC) design workflows.

This procurement directly supports **Component I** and **Component II** of the NSHRDP framework, namely:

- **Component I: Semiconductor Education and Research Clusters (SERCs):** Focused on formal academic training, this component aims to strengthen undergraduate (BS/BE) and postgraduate (MS/PhD) programs in electronics, electrical engineering, and computer engineering disciplines, through access to state-of-the-art Electronic Design Automation (EDA) tools and compute infrastructure.

- **Component II: Upskilling Training Programs (USTPs):** Targeting professionals and industry practitioners, this component seeks to deliver fast-track, modular training in digital and analog chip design, verification, layout, and embedded systems, aligned with real-world industrial demands.

Licensing Expectations

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- **Licenses:** The bidder shall provide 150 academic licenses for use across Components I & II Semiconductor Education & Research Clusters (SERCs), covering academic, research, and continuing-education activities, for a period of three (03) years.